

Abstract of the Disclosure:

A half-bridge circuit includes: a vertically designed n-conducting first MOS transistor that is integrated in a first semiconductor body having a front side and a rear side; and a
5 vertically designed p-conducting second MOS transistor that is integrated in a second semiconductor body having a front side and a rear side. The first and second transistors are connected in series between a first connection terminal and a second connection terminal. The half-bridge circuit also
10 includes a drive circuit for driving the first and second transistors. The first and second transistors are applied to a common connection plate.

MPW/vs